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**Amendments To The Claims:**

This listing of claims will replace all prior versions and listing of claims for this application:

**Listing of Claims:**

1. (Currently amended) A semiconductor assembly comprising:

a support structure having a top surface, ~~wherein said support structure is a film;~~ and

at least one semiconductor die having a perimeter, including four sides, and a top and bottom surface, said bottom surface having a smaller area than said top surface of said support structure, said at least one semiconductor die being secured at its bottom surface to said top surface of said support structure solely by a flowable adhesive material which does not extend past any one of the sides of said perimeter of said at least one semiconductor die, said top surface of said support structure having at least one electrical contact area at a distance outside said perimeter of said at least one semiconductor die, said at least one semiconductor die being in electrical communication with said at least one electrical contact area.

Claims 2-3 (Canceled).

4. (Original) The semiconductor assembly of claim 1, wherein said support structure is at least one semiconductor die with a top and bottom surface.

5. (Original) The semiconductor assembly of claim 1, wherein said flowable adhesive material is an epoxy.

6. (Original) The semiconductor assembly of claim 1, wherein said flowable adhesive material covers an area less than or equal to about 90 % of said at least one semiconductor die bottom surface area.

7. (Original) The semiconductor assembly of claim 6, wherein said flowable adhesive material covers an area greater than or equal to about 50 % of said at least one semiconductor die bottom surface area.

8. (Currently amended) The semiconductor assembly of claim 1, wherein ~~said top surface of said support structure has at least one electrical contact area and~~ a distance between ~~an~~said electrical contact area and said perimeter of said at least one semiconductor die is less than or equal to about 428 microns.

9. (Currently amended) The semiconductor assembly of claim 8, wherein a distance between ~~an~~said electrical contact area and said perimeter of said at least one semiconductor die is less than or equal to about 200 microns.

Claim 10 (Canceled)

11. (Currently amended) The semiconductor assembly of claim ~~10~~1, wherein said electrical communication is through a wire bond.

12. (Original) The semiconductor assembly of claim 10, wherein said at least one electrical contact area is a bonding pad.

13. (Currently amended) The semiconductor assembly of claim ~~10~~1, further comprising an encapsulating material for encapsulating said die, electrical communication, and at least a portion of said support structure.

14. (Original) The semiconductor assembly of claim 13, wherein said encapsulating material fills in at least some portion of a space between said bottom surface of said die and said top surface of said support structure.

15. (Previously presented) A semiconductor assembly comprising:

a first semiconductor die having a top and a bottom surface;

a second semiconductor die having a perimeter, including four sides, and a top and bottom surface, said bottom surface having a smaller area than said top surface of said first semiconductor die, said second die being secured at its bottom surface to said top surface of said first semiconductor die by a flowable adhesive material which does not extend past any one of the sides of said perimeter of said second semiconductor die; and

wherein said top surface of said first semiconductor die has at least one electrical contact area positioned at a location exterior to said perimeter of said second semiconductor die, and wherein a distance between said electrical contact area and said perimeter of said second semiconductor die is less than or equal to about 428 microns.

16. (Original) The semiconductor assembly of claim 15, wherein said first semiconductor die is secured to a support structure.

17. (Original) The semiconductor assembly of claim 16, wherein said support structure is a film.

18. (Original) The semiconductor assembly of claim 16, wherein said support structure is a printed circuit board.

19. (Original) The semiconductor assembly of claim 15, wherein said flowable adhesive material is epoxy.

20. (Original) The semiconductor assembly of claim 15, wherein said flowable adhesive material covers an area less than or equal to about 90% of said second semiconductor die's bottom surface area.

21. (Original) The semiconductor assembly of claim 20, wherein said flowable adhesive material covers an area greater than or equal to about 50% of said second semiconductor die's bottom surface area.

Claims 22-31 (Canceled).

32. (Withdrawn) A method of manufacturing a semiconductor assembly comprising:

depositing a flowable adhesive material on a bottom surface of a semiconductor die;

providing a supporting structure for said at least one semiconductor die, wherein said supporting structure has a perimeter greater than a perimeter of said at least one semiconductor die and has at least one electrical contact area located adjacent an edge of said semiconductor die; and

applying a force between said at least one semiconductor die and said supporting structure causing said flowable adhesive to flow but not extend past the perimeter of said at least one semiconductor die.

33. (Withdrawn) The semiconductor assembly of claim 32, wherein, said flowable adhesive material covers an area greater than or equal to about 50% and less than or equal to about 90% of said bottom surface of said at least one semiconductor die.

34. (Withdrawn) A method of forming a semiconductor assembly comprising the steps of:

providing a first semiconductor die having a top and bottom surface and at least one electrical contact area adjacent an edge of said top surface;

providing a second semiconductor die having a top and bottom surface;

depositing a flowable adhesive material on said top surface of said first die so that said flowable adhesive material covers an area no less than or equal to about 50% and no greater than or equal to about 90% of said second die bottom surface area;

pressing said second die against said first die so that said flowable adhesive material flows but does not extend past the perimeter of said second die.

35. (Withdrawn) The method of claim 34, further comprising the steps of securing said bottom surface of said first die to a support structure.

36. (Withdrawn) The method of claim 35, where said support structure is a printed circuit board.

37. (Withdrawn) The method of claim 35, where said support structure is a film.

38. (Withdrawn) The method of claim 34, wherein said flowable adhesive material is an epoxy.

39. (Withdrawn) A method of forming a semiconductor assembly comprising the steps of:

providing a first semiconductor die having a top and bottom surface and at least one electrical contact area adjacent an edge of said top surface;

providing a second semiconductor die having a top and bottom surface;

depositing a flowable adhesive material on said bottom surface of said second die so that said flowable adhesive material covers an area greater than or equal to about 50% and less than or equal to about 90% of said second die bottom surface area;

pressing said second die against said first die so that said flowable adhesive material flows out but does not extend past the perimeter of said second die.

40. (Currently amended) A semiconductor assembly comprising:

a support structure having a top surface; and

a first semiconductor die having a perimeter, including four sides, and a top and bottom surface, said bottom surface having a smaller area than said top surface of said support structure, said first semiconductor die being secured at its bottom surface to said top surface of said support structure by a compressed flowable adhesive material which does not extend past any one of the sides of said perimeter of said at least one semiconductor die such that there is a first cavity along at least a portion of said perimeter between said support structure and said first semiconductor die, said first cavity being filled

with an encapsulating material, such that only said adhesive material and said encapsulating material are between said first semiconductor die and said support structure, said top surface of said support structure having at least one electrical contact area at a distance outside said perimeter of said at least one semiconductor die, said at least one semiconductor die being in electrical communication with said at least one electrical contact area.

41. (Currently amended) The semiconductor assembly of claim 40 further comprising a second semiconductor die having a perimeter, including four sides, and a top and bottom surface, said bottom surface having a smaller area than said top surface of said first semiconductor die, said ~~first~~second semiconductor die being secured at its bottom surface to said top surface of said first semiconductor die by a compressed flowable adhesive material which does not extend past any one of the sides of said perimeter of said second semiconductor die such that there is a second cavity along at least a portion of said perimeter between said first semiconductor die and said second semiconductor die, said second cavity being filled with said encapsulating material.

42. (Currently amended) A semiconductor assembly comprising:

a support structure having a top surface; and

at least one semiconductor die having a perimeter, including four sides, and a top and bottom surface, said bottom surface having a smaller area than said top surface of said support structure, said at least one semiconductor die being secured at its bottom surface to said top surface of said support structure by a flowable adhesive material which does not extend past any one of the sides of said perimeter of said at least one semiconductor die, said flowable adhesive material covering an area greater than or equal to about 50% of said at least one semiconductor die's bottom surface area, said top surface of said support structure having at least one electrical contact area at a distance outside said perimeter of said at least one semiconductor die, said at least one semiconductor die being in electrical communication with said at least one electrical contact area.